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FIG. 1A

07/27/14

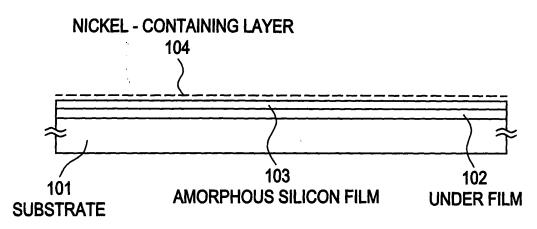


FIG. 1B

LASER CRYSTALLIZATION STEP

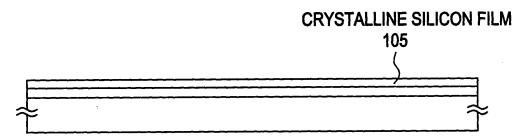


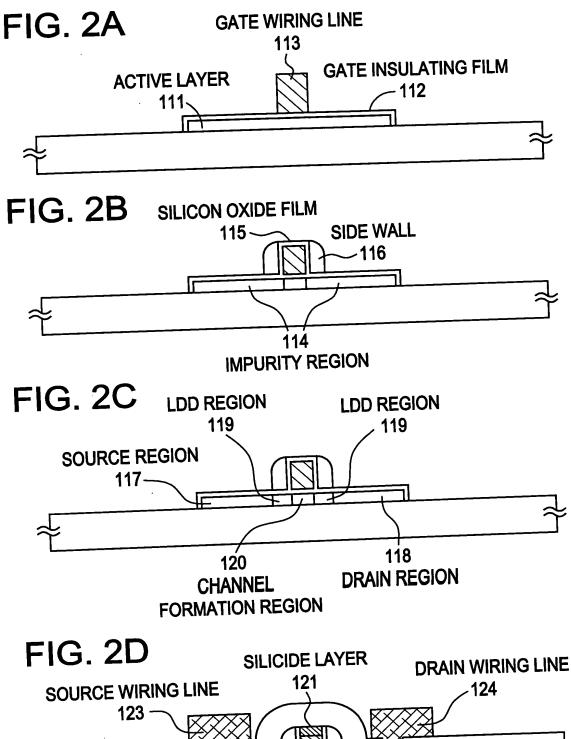
FIG. 1C

THERMAL TREATMENT STEP IN REDUCING ATMOSPHERE

CRYSTALLINE SILICON FILM

106

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122

INTERLAYER

INSULATING

121

SILICIDE LAYER

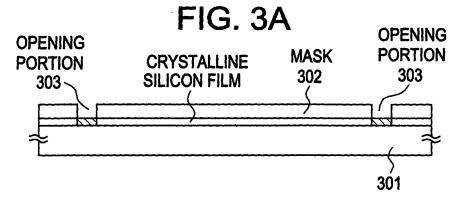
115

SILICIDE LAYER

2015.10 ...

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FIG. 3B

ADDING STEP OF PHOSPHORUS

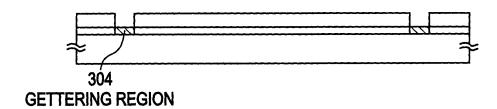


FIG. 3C

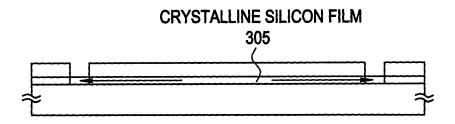
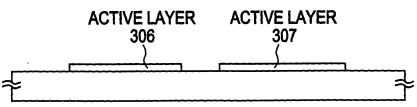


FIG. 3D

HEAT TREATMENT STEP IN REDUCING ATMOSPHERE



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FIG. 4A

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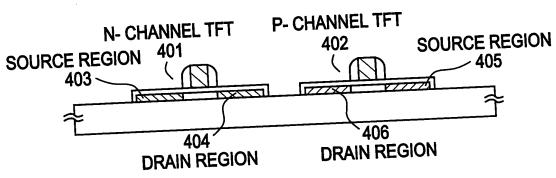
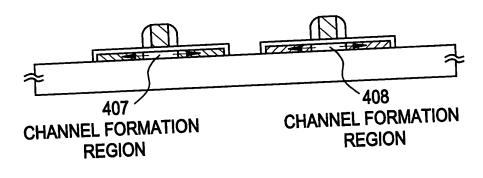
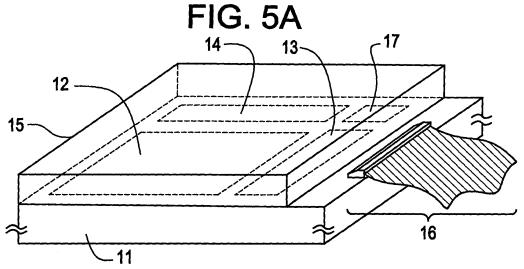


FIG. 4B GETTERING STEP



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- 11: SUBSTRATE HAVING INSULATING SURFACE
- 13: SOURCE DRIVER CIRCUIT
- 15: OPPOSITE SUBSTRATE
- 17: SIGNAL PROCESSING CIRCUIT

12: PIXEL MATRIX CIRCUIT

24.

14: GATE DRIVER CIRCUIT

16: FPC

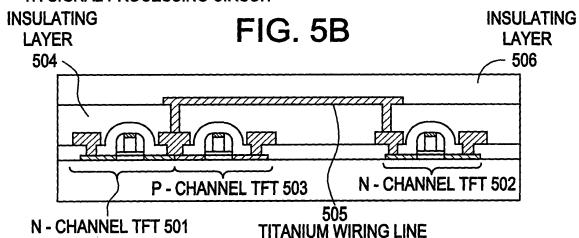
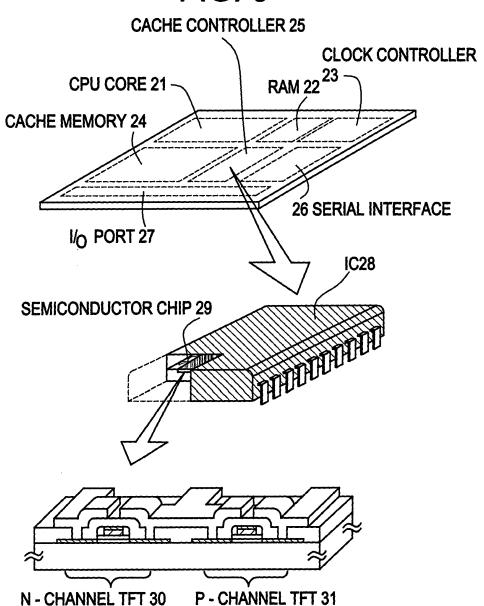


FIG. 5C
TITANIUM
PIXEL
INSULATING LAYER
505
509
FIG. 5C
INSULATING
LAYER
504
FIG. 5C
INSULATING
LAYER
504
FIG. 5C
INSULATING
LAYER
504
FIG. 5C
INSULATING
LAYER
507
FIG. 5C
INSULATING
LAYER
504
FIG. 5C
INSULATING
LAYER
504
FIG. 5C
INSULATING
LAYER
507
CAPACITANCE

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FIG. 6

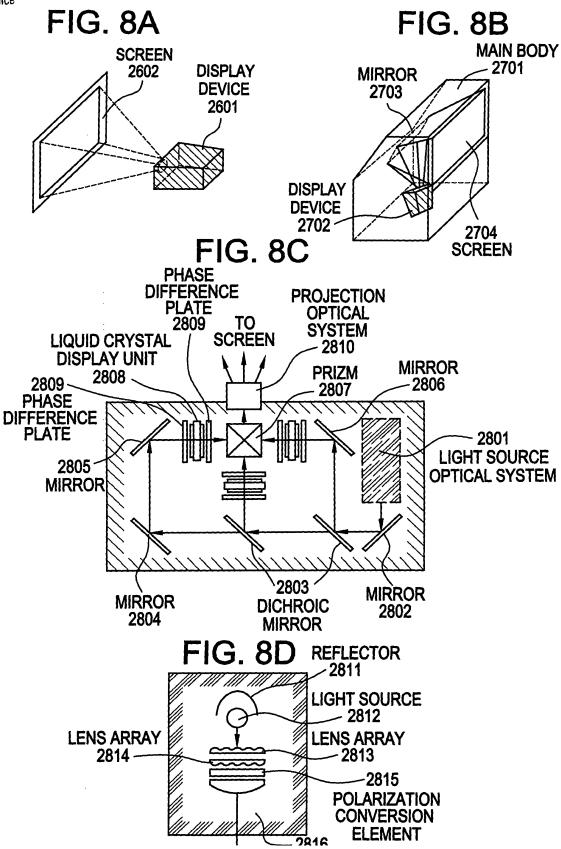


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FIG. 7B FIG. 7A **IMAGE RECEIVING PORTION** 2106 **IMAGE INPUT PORTION** MAIN BODY MAIN BODY 2001 2002 **OPERATION SWITCH** 2101 2104 **DISPLAY DEVICE AUDIO INPUT** PORTION 2003 2103 **KEYBOARD** 2004 2102 DISPLAY DEVICE 2105 **BATTERY** FIG. 7C FIG. 7D MAIN BODY **DISPLAY DEVICE** 2201 ARM PORTION **CAMERA PORTION** MAIN BODY 2205 2303 2301 2202 2203 **IMAGE RECEIVING PORTION** 2302 **DISPLAY DEVICE** 2204 OPERATION SWITCH FIG. 7F FIG. 7E DISPLAY DEVICE EYEPIECE PORTION 2503 2402 2401 MAIN BODY **OPERATION SWITCH** MAIN BODY 2504 2501 SPEAKER PORTION - 2403 RECORDING MEDIUM 2404 OPERATION SWITCH 2502

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FIG. 9A

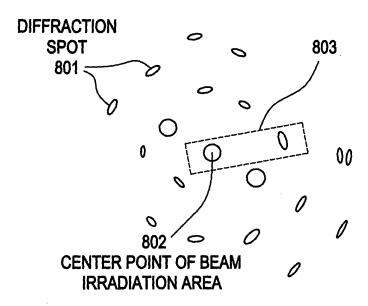
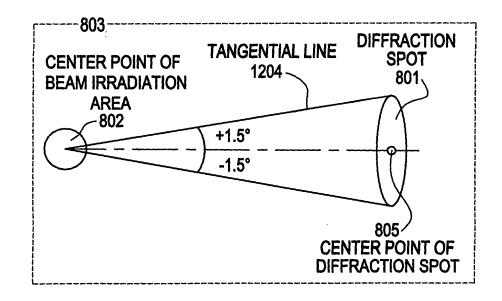


FIG. 9B



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FIG. 10

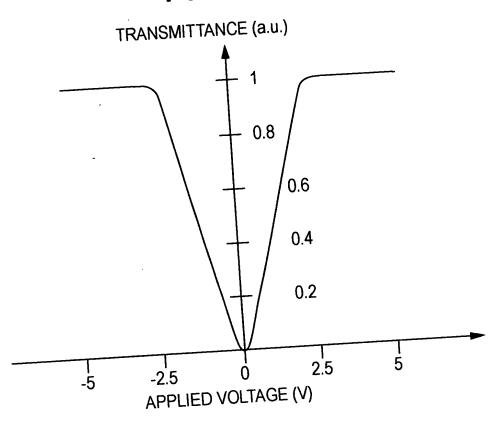
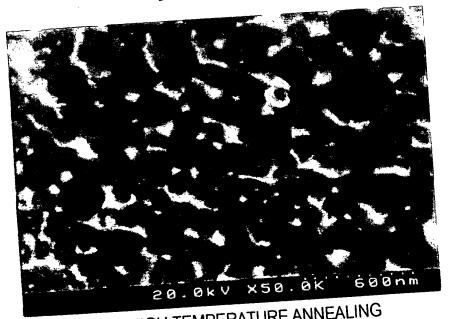


FIG. 11



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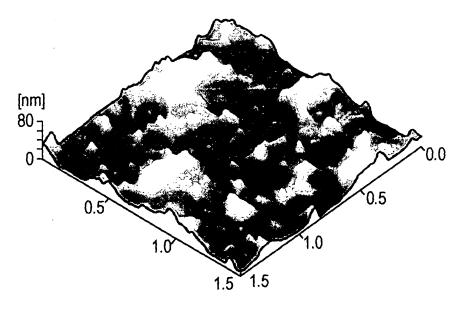
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FIG. 12



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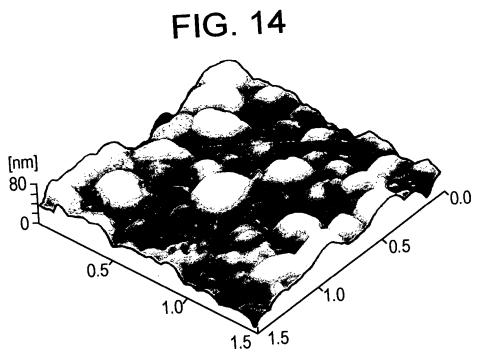
FIG. 13



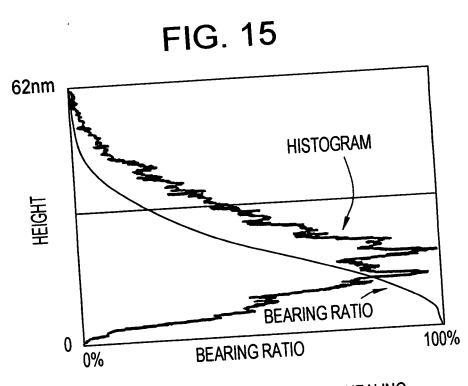
BEFORE HIGH TEMPERATURE ANNEALING

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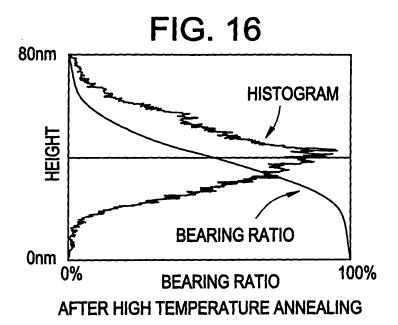


AFTER HIGH TEMPERATURE ANNEALING



BEFORE HIGH TEMPERATURE ANNEALING

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FIG. 17

OBSERVATION REGION	BEFORE HIGH TEMPERATURE ANNEALING	AFTER HIGH TEMPERATURE ANNEALING
1	13.623	40.925
2	20.027	51.126
3	20.629	59.364
4	21.798	48.539
5	16.666	55.341
6	15.097	46.510
7	13.120	57.655
8	14.035	51.120
9	12.599	54.416
10	20.699	36.945
MINIMUM (%)	12.60	36.95
MAXIMUM (%) VALUE	21.80	59.36
AVERAGE (%) VALUE	16.83	50.19
STANDARD DEVIATION	3.61	7.18